

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert M. Wolrich et al.

Art Unit : Unknown

Serial No. : Not Yet Assigned

Examiner : Unknown

Filed : March 16, 2001

Title : **LOCAL REGISTER INSTRUCTION FOR MICRO ENGINE USED IN
MULTITHREADED PARALLEL PROCESSOR ARCHITECTURE**

Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as follows:

The Title:

Please amend the title throughout to read:

“REGISTER INSTRUCTIONS FOR A MULTITHREADED PROCESSOR”

In the specification:

On page 11, between lines 6-17, please add the following paragraph to the ‘Detailed Description’:

-- The EBOX also includes two multiplexors, “mux A” and “mux B”, which provide a selection capability of the operands to be input to ALU 76a. The 32-bit wide output buses from several microengine data sources are connected to mux A and mux B, including an IMMEDIATE_DATA bus from the control logic 72, a write-back data bypass bus from the

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output of ALU 76a, and a data bus from read register bank 80. Also, an output bus from Bank A of general-purpose register 76b is connected to input mux A and an output bus from Bank B of general-purpose register 76b is connected to input mux B. All of the bus inputs to mux A and mux B are 32 bits wide. Control logic is provided to mux A and mux B to allow selection of individual 8-bit bytes of each operand transferred through to ALU 76a, as explained below in reference to local register instructions.--

REMARKS

Applicant has amended the title of the continuing application to better reflect the invention as claimed. This amendment to the title does not add new matter.

Applicant has also amended the detailed description. This amendment does not add new matter, it merely describes the elements included in the EBOX as shown in FIG. 3. The additional paragraph is supported by the drawings filed with the original application (this application is a continuation application of and claims priority to PCT Application Serial No. PCT/US00/24054 filed on September 1, 2000). Additional support for the amendment is found at page 12, lines 17-24, of the specification, which describes the controlling and shifting of 8-bit bytes within a 32-bit operand.

Applicant submits that the specification and claims are now in condition for examination, which action is requested. Filed herewith is a check in payment required by the above amendment. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 3/19/2001



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